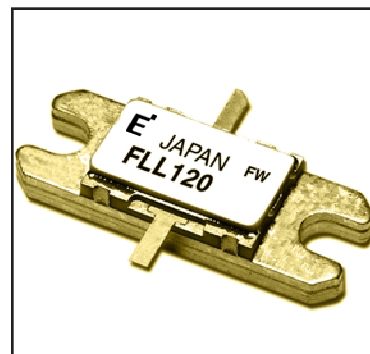


### FEATURES

- High Output Power:  $P_{1dB} = 40.0\text{dBm}$  (Typ.)
- High Gain:  $G_{1dB} = 10.0\text{dB}$  (Typ.)
- High PAE:  $\eta_{add} = 40\%$  (Typ.)
- Proven Reliability
- Hermetically Sealed Package



### DESCRIPTION

The FLL120MK is a Power GaAs FET that is specifically designed to provide high power at L-Band frequencies with gain, linearity and efficiency superior to that of silicon devices. The performance in multitone environments for Class AB operation make them ideally suited for base station applications.

Eudyna stringent Quality Assurance Program assures the highest reliability and consistent performance.

### ABSOLUTE MAXIMUM RATING (Ambient Temperature $T_a=25^\circ\text{C}$ )

Item	Symbol	Condition	Rating	Unit
Drain-Source Voltage	$V_{DS}$		15	V
Gate-Source Voltage	$V_{GS}$		-5	V
Total Power Dissipation	$P_T$	$T_C = 25^\circ\text{C}$	37.5	W
Storage Temperature	$T_{stg}$		-65 to +175	$^\circ\text{C}$
Channel Temperature	$T_{ch}$		175	$^\circ\text{C}$

Eudyna recommends the following conditions for the reliable operation of GaAs FETs:

1. The drain-source operating voltage ( $V_{DS}$ ) should not exceed 10 volts.
2. The forward and reverse gate currents should not exceed 26.8 and -5.8 mA respectively with gate resistance of  $50\Omega$ .
3. The operating channel temperature ( $T_{ch}$ ) should not exceed  $145^\circ\text{C}$ .

### ELECTRICAL CHARACTERISTICS (Ambient Temperature $T_a=25^\circ\text{C}$ )

Item	Symbol	Test Conditions	Limit			Unit
			Min.	Typ.	Max.	
Saturated Drain Current	$I_{DSS}$	$V_{DS} = 5\text{V}, V_{GS} = 0\text{V}$	-	4000	6000	mA
Transconductance	$g_m$	$V_{DS} = 5\text{V}, I_{DS} = 2400\text{mA}$	-	2000	-	mS
Pinch-off Voltage	$V_p$	$V_{DS} = 5\text{V}, I_{DS} = 240\text{mA}$	-1.0	-2.0	-3.5	V
Gate Source Breakdown Voltage	$V_{GSO}$	$I_{GS} = -240\mu\text{A}$	-5	-	-	V
Output Power at 1dB G.C.P.	$P_{1dB}$	$V_{DS} = 10\text{V}$ $I_{DS} = 0.55 I_{DSS}$ (Typ.), $f = 2.3\text{GHz}$	39.5	40.0	-	dBm
Power Gain at 1dB G.C.P.	$G_{1dB}$		9.0	10.0	-	dB
Power-added Efficiency	$\eta_{add}$		-	40	-	%
Thermal Resistance	$R_{th}$	Channel to Case	-	3.3	4.0	$^\circ\text{C}/\text{W}$

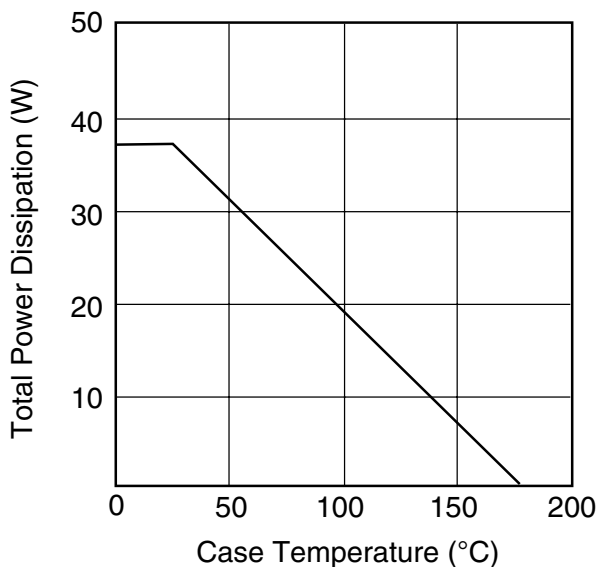
CASE STYLE: MK

G.C.P.: Gain Compression Point

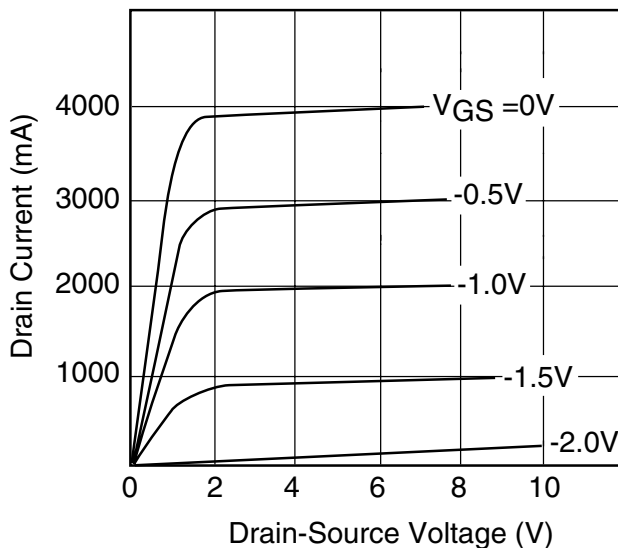
# FLL120MK

## L-Band Medium & High Power GaAs FET

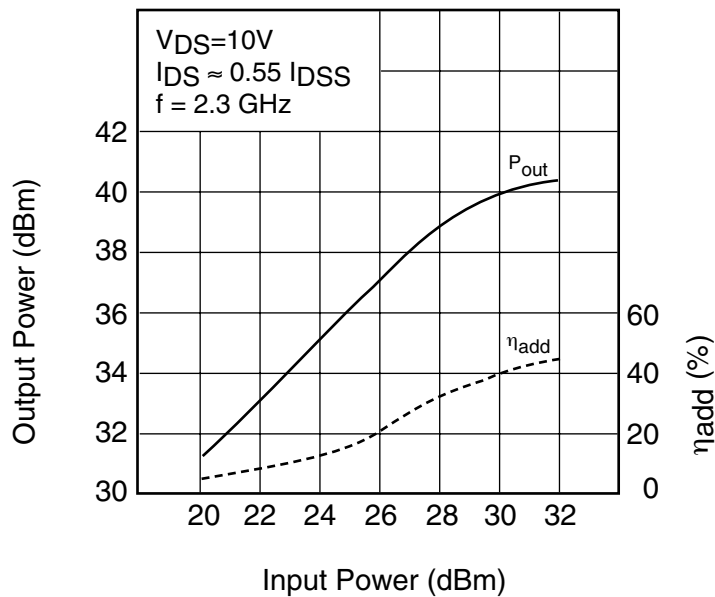
### POWER DERATING CURVE

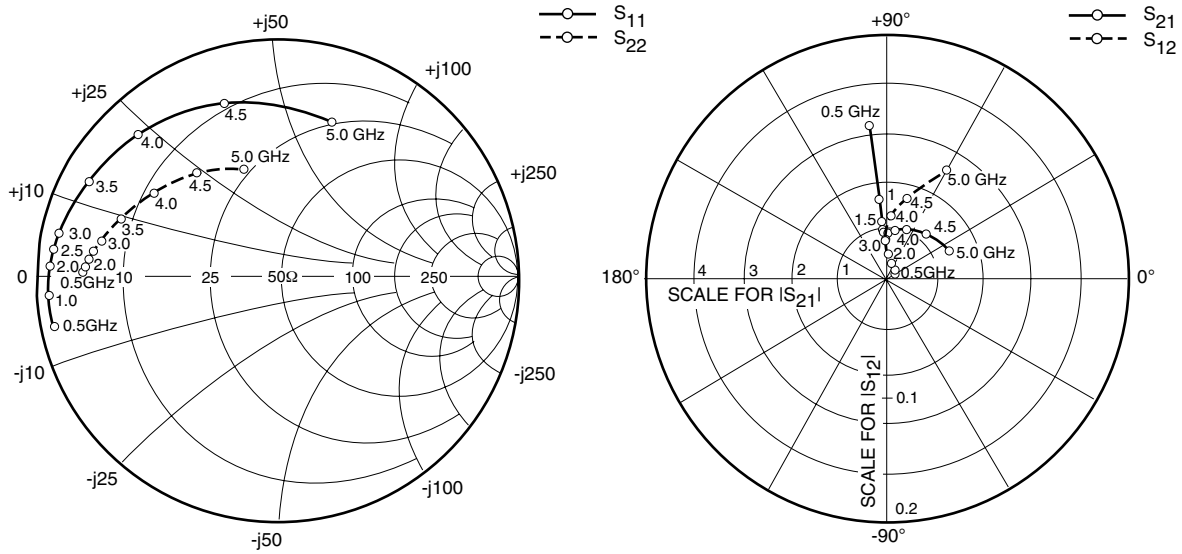


### DRAIN CURRENT vs. DRAIN-SOURCE VOLTAGE



### OUTPUT POWER vs. INPUT POWER





### S-PARAMETERS

$V_{DS} = 10V, I_{DS} = 2200mA$

FREQUENCY (MHZ)	S11		S21		S12		S22	
	MAG	ANG	MAG	ANG	MAG	ANG	MAG	ANG
500	.959	-168.4	3.136	95.8	.008	31.0	.824	179.4
1000	.953	-176.0	1.617	94.1	.010	45.7	.813	178.8
1500	.953	-179.6	1.170	93.8	.011	64.3	.810	177.7
2000	.951	177.0	.978	92.3	.014	82.4	.792	176.5
2500	.939	172.6	.927	91.4	.021	89.1	.778	174.0
3000	.914	165.1	.936	88.0	.024	93.2	.739	168.3
3500	.885	152.7	.990	80.6	.033	94.6	.695	158.9
4000	.836	134.0	1.106	67.1	.051	88.1	.633	145.1
4500	.766	107.3	1.239	48.2	.067	77.3	.559	128.0
5000	.690	71.6	1.415	23.9	.103	60.5	.477	107.3

# FLL120MK

## L-Band Medium & High Power GaAs FET

### Case Style "MK" Metal-Ceramic Hermetic Package

